Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1517	self-timed and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 16:07
L2	649	1 and address adj decod$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 16:07
L3	5159	wordline adj driver or word adj line adj driver	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 16:07
L4	1040	read adj delay	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 16:08
L5	94	sense adj amplifier adj delay	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 16:08
L6	111	read adj margin and adjustment	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 16:08
L7	1	2 and 3 and 4 and 5 and 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 16:09
L8	1	2 and 3 and 4 and 5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 16:10

L9	1	2 and 3 and 4 and 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 16:10
L10	181	2 and 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 16:10
L11	2	10 and 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 16:10
L12	0	10 and sense adj amplifier adj dealy	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 16:10
L13	1	10 and sense adj amplifier adj delay	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 16:11
L14	95	10 and sense adj amplifier with delay	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 16:13
L15	1	14 and sense adj amplifier adj driver	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 16:13
L16	49	14 and sense adj amplifier near3 driver	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 16:33

L17	1713	365/194.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 16:33
L18	5	16 and 17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 16:46
L19	0	read adj delay adj adjustment adj circuit with sense adj amplifier adj delay adj circuit with read adj margin adj adjustment adj circuit. clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/05 16:48